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SERIAL NUMBER 10/731,072	FILING DATE 12/09/2003 RULE	CLASS 324	GROUP ART UNIT 2829	ATTORNEY DOCKET NO. ROC920030297US1
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APPLICANTS

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** CONTINUING DATA *****

None at

** FOREIGN APPLICATIONS *****

None ✓

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 03/11/2004

Foreign Priority claimed	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR COUNTRY	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance	VT	DRAWING 4	CLAIMS 18	CLAIMS 2
Verified and Acknowledged <i>Robert R. Williams</i> Examiner's Signature	Lifland				

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TITLE

Method and testing circuit for tracking transistor stress degradation

FILING FEE RECEIVED 770	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit
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